



TESTA 2018

3rd International Test Standards Application Workshop

Thursday May 31st – June 1st, 2018 – Bremen (Germany)

<https://www.hshl.de/en/testa2018/>

Call for Contributions

General Chair:

René Krenz-Baath – Hamm-Lippstadt University (UAS)

Past Chair:

Artur Jutman – Testonica Lab

Program Chairs:

Michele Portolan – TIMA Laboratory
Tom Waayers – NXP Semiconductors

Publication Chair:

Michael Kochte – University Stuttgart

Panel Chairs:

Erik Larsson – Lund University
Al Crouch – Amida Technology Solutions

Embedded Tutorial Chair:

Daniel Tille - Infineon Technologies
Heiko Ehrenberg – Goepel Electronics

Advertising and Sponsorship:

Martin Keim – Mentor Graphics

Organizing Committee:

Al Crouch – Amida Technology Solutions
Heiko Ehrenberg – Goepel Electronics
Artur Jutman – Testonica Lab
Martin Keim – Mentor Graphics
Michael Kochte – TIMA Laboratory
Rene Krenz - Baath Hamm-Lippstadt University (UAS)
Erik Larsson – Lund University
Michele Portolan – TIMA Laboratory
Ivo Steverink – JTAG Technologies
Tom Waayers – NXP Semiconductors
Daniel Tille - Infineon Technologies

Scope and Mission

The TESTA workshop is a focused, open discussion platform dedicated to exchange of fresh ideas, industry best practices, methodologies and work-in-progress around chip test, board test and system test related standards, especially those being actively developed today or the ones recently released.

Contributions covering IEEE 1149.x, IEEE 1500, IEEE 1687, IEEE P1838, IEEE 1450, and others are anticipated. Particularly welcome are contributions that discuss what works, what does not, or how the standards were incorporated into existing or upcoming DFT methodologies. Reports on first-time usage of new and upcoming standards are welcome, as is research exploring the best usage of features described in these standards.

Cyber security aspects in the area of test are of special interest at this year's workshop.

Format of the Workshop

The workshop is co-located with ETS'2018. The program will be composed of regular presentations and posters. Embedded tutorials dedicated to educating the attendees will be also incorporated into the program. *The format will encourage brainstorming and networking rather than dry reporting.*

Submissions

The organizers invite new work within the scope of the workshop. Paper submissions are welcome as an **extended abstract of 1-2 pages**. A formal tutorial proposal shall include a one-page summary describing the contents and goals of the proposed tutorial.

A submission will be considered commitment that upon acceptance the author(s) will present the paper or tutorial at the workshop. Submission details can be found on the workshop's web page.

Please submit your extended abstract or paper using the EasyChair system at <https://easychair.org/conferences/?conf=testa18>.

Informal Proceedings

The workshop distributes manuscripts and slideware only to attendees after the workshop. No author is obligated to have his or her contribution included in these informal proceedings. A paper manuscript may be a revised extended abstract or in common IEEE 2-column format up to 6 pages.

For further information, contact rene.krenz-baath@hshl.de or see the webpage at <https://www.hshl.de/en/testa2018>.

Key Dates – Embedded Tutorials and Paper Submissions

Extended Submission deadline	March 15th
Notification of acceptance	March 26th
Camera ready slideware	May 24th
Camera ready paper (optional)	June 2nd